

PHISON

- PCIe Gen5
- Compliant with CXL 2.0
- 16 lanes supporting link bifurcation
- Conforming to Intel Retimer Standard Footprint
- Reach extended by 42dB at 16GHz
- 4-level I/O for EQ/GAIN parameter-setting
- 2-dimension frequency-setting
- Low-latency modes (<5ns)
- SMBus configuration with external EEPROM
- Diagnostic debug info logging
- Integrated AC coupling capacitors
- -40°C ~ +125°C junction temperature

PS7201

Gear Up for New Age of High Speed Transmission

